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(43)

2002-0041633  
2002 06 03

(73)

20 LG

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45 3 103 403

(74)

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(54)

가 , 가

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1

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3

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100:

200:

300:

400:

500:

600:

(FPGA:FIELD PROGRAMMABLE ARRAY) 가

1 (DT0~DT7) (10) ;  
 (11) (WREN) (12) ; (11) (BYTE0\_SEL) (10) 가 (DT0~DT7)  
 (BYTE0\_SEL) (12) ; (12) (RDT0~RDT7) (13) ;  
 10) (DATA0~DATA7) (13) ; (11) (RDT0~RDT7) (13) ;  
 (DT0~DT7) (12) 가 (11) (BYTE0\_SEL) (BYTE0  
 \_SEL) (12) 가 (12) (BYTE0\_SEL)  
 (WREN) (DT0~DT7) (12) (DT0~DT7)가,  
 (12) (10) (12)  
 (13) (12) (DATA0~DATA7)  
 (12) (11) (RDEN) (BYTE0\_SEL)  
 (10) (RDT0~RDT7)  
 (11) (DT0~DT7) (11)  
 (DATA0~DATA7) 가 (11)

가

가

가

가

2  
 (600) ; (DT0~DT7) (600) (PARITY) (100) ; (EX1),(EX2) (200) (BYTE0\_S  
 EL) 가 (WREN) (PARITY)가 (3  
 (100) (200) (DT0~DT7) (BYTE0\_SEL) (WREN) (400) ;  
 (SYSCLK) (400) (300) (5  
 DATA0~DATA7) (PARITY) (200)  
 (600) 4 (DT0~DT3),(DT4~DT7)  
 1, 2 (EX1),(EX2) ; 1, 2 (EX1),(EX2)  
 3 (EX3)  
 (400) (BYTE0\_SEL) (WREN)  
 1 (AN1) ; 1 (AN1) 가 (SYDCLK)  
 (DF10) ; 1 (DF10) 가  
 (SYSCLK) 2 (DF11) ; 2 (DF11)  
 (DF11) 가 3 (DF12) ;  
 2 (AN2) ; 3 (DF12) 가 (SYSCLK)  
 (AN2) ; 4 (AN2) 가 (SYSCLK)  
 (DF13)  
 (500) ((300) (DATA0~D  
 ATA3) 1 (EX4) ; (30  
 0) (DATA4~DATA7) (PARITY)  
 2 (EX5) ; 1, 2 (EX4),(EX5)  
 3 (EX6) ; 3 (EX6)  
 가 (400) (CHK\_CLK)  
 (DF14)  
 가 (600) 8 (DT0~DT7)  
 1, 2 (PARITY) (EX1),(EX2) (DT0~DT3) ,(DT4~DT7) 4  
 3 (EX3) 1, 2 (EX  
 1),(EX2) (PARITY)  
 (100) (600) (PARITY) (EX1),(  
 EX2) (300) (200)  
 (BYTE0\_SEL) 가 (WREN) (PARITY)가  
 (100) (300) (DT0~DT7) 가 2  
 (300) (400)  
 (200) (BYTE0\_SEL) (WREN) (SYSCLK)  
 (AN1) (200) 3 (a) (BYTE0\_SEL) 3 (b)  
 (WREN) 가 (SYDCLK) (DF10) 3 (c) (AN1)  
 (DF10) 가 (SYSCLK) (DF11) 3 (d)  
 CLK) (DF12) (DF11) 가 (SYS  
 3 (e) (AN2) (DF11) (DF12)  
 3 (f) (DF13) (AN2)  
 가 (SYSCLK) (DF13) (AN2)  
 (300) (500) (400)  
 (300) (DATA 0~DATA7) (PARITY)  
 (EX4~EX6) (300) (DT0~DT7) (DF14)

(400) (DT0~DT7)가 (CHK\_CLK)가  
 (DT0~DT7)가 (CHK\_CLK)가  
 (300) (CHK\_CLK) (DT0~DT7)가  
 (200)

가 .

(57)

1.

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 ; 가 , 가 ;  
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2.

1 , ; 1, 2 1, 2 3

3.

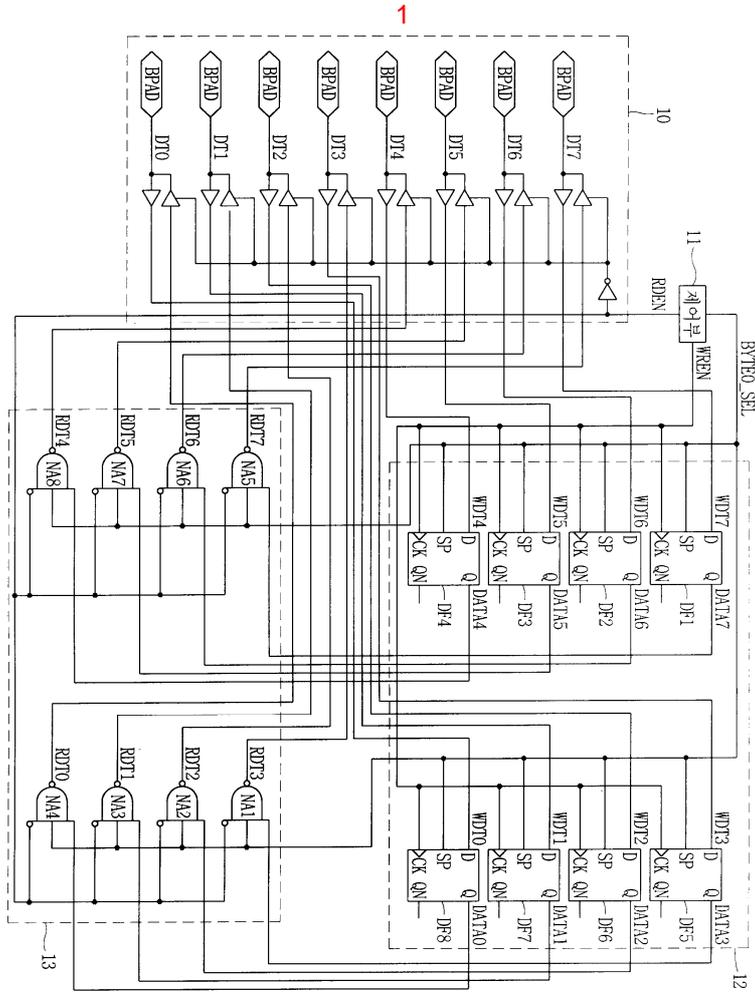
1 , ; 가 가 ;  
 1 가 가 1 2 3 ;  
 ; 2 가 3 4  
 ; 가

4.

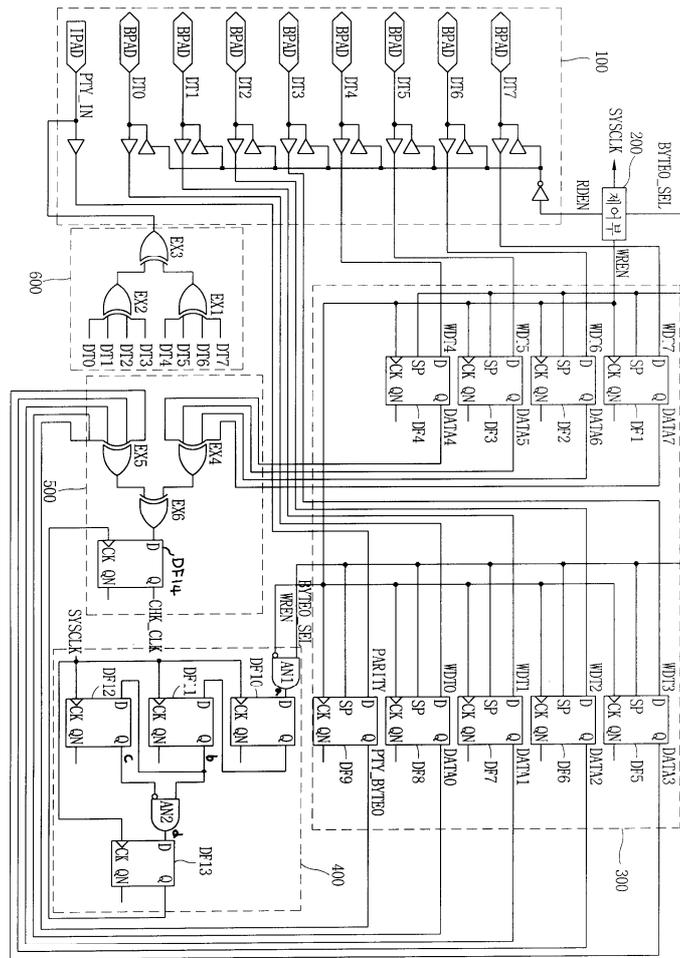
1 , 1 ; 2 3 ; 1, 2 3  
 가 3 ; 3

5.

1 4 , 가 가



2



3

- (a) BYTE\_SEL
- (b) WREN
- (c) @
- (d) b
- (e) c
- (f) d

