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(71) Applicant and

(72) Inventor: YOO, Myung Cheol [KR/US]; 614 Abbie Court, Pleasanton, CA 94566 (US).

(74) Agent: ASHBY, David, C.; IPSG, P.C., 10121 Miller Avenue, Suite 201, Cupertino, CA 95014 (US).

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(54) Title: METHOD OF FABRICATING VERTICAL STRUCTURE COMPOUND SEMICONDUCTOR DEVICES

(57) Abstract: A method of fabricating a vertical structure opto-electronic device includes fabricating a plurality of vertical structure opto-electronic devices on a crystal substrate, and then removing the substrate using a laser lift-off process. The method then fabricates a metal support structure in place of the substrate. In one aspect, the step of fabricating a metal support structure in place of the substrate includes the step of plating the metal support structure using at least one of electroplating and electro-less plating. In one aspect, the vertical structure is a GaN-based vertical structure, the crystal substrate includes sapphire and the metal support structure includes copper. Advantages of the invention include fabricating vertical structure LEDs suitable for mass production with high reliability and high yield.

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C. DOCUMENTS CONSIDERED TO BE RELEVANT				
Category *	Citation of document, with indication, where a	ppropriate, of the relevant passages	Relevant to claim No.	
X	US 6,562,648 B1 (WONG et al) 13 May 2003 (13.05.2003), column 4, line 47 - column 5, 1, 3, 5, 7, 13, 14, 18 line 56.		1, 3, 5, 7, 13, 14, 18, 19, 21	
Y Y			2, 4, 6, 8, 20 2, 4, 6, 8, 20	
Further	documents are listed in the continuation of Box C.	See patent family annex.		
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Commissioner for Patents P.O. Box 1450		Zandra Smith		
Alexandria, Virginia 22313-1450 Facsimile No. (571) 273-3201		Telephone No. (571) 273-8300		

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