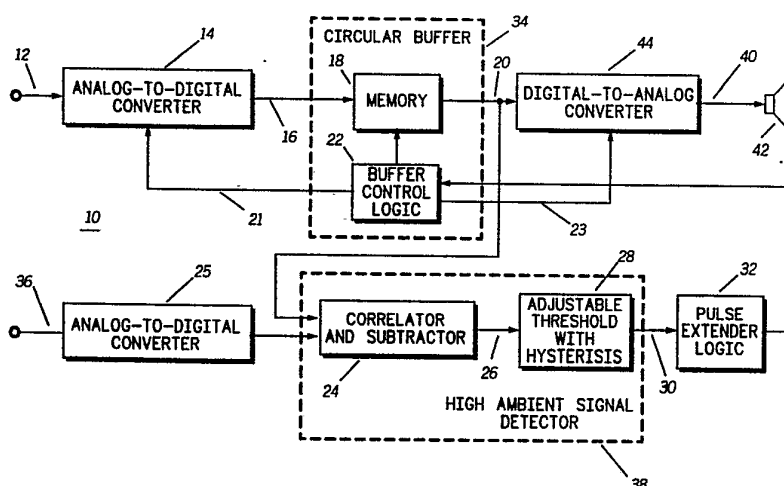




INTERNATIONAL APPLICATION PUBLISHED UNDER THE PATENT COOPERATION TREATY (PCT)

<p>(51) International Patent Classification ⁵ : H03G 3/20</p>	<p>A1</p>	<p>(11) International Publication Number: WO 91/14309</p> <p>(43) International Publication Date: 19 September 1991 (19.09.91)</p>
<p>(21) International Application Number: PCT/US91/00873</p> <p>(22) International Filing Date: 12 February 1991 (12.02.91)</p> <p>(30) Priority data: 489,144 5 March 1990 (05.03.90) US</p> <p>(71) Applicant: MOTOROLA, INC. [US/US]; 1303 East Algonquin Road, Schaumburg, IL 60196 (US).</p> <p>(72) Inventors: SCHORMAN, Eric, R. ; 1304 Wedglea Drive, Bedford, TX 76021 (US). LAIRD, Kevin, M. ; 5244 Dunson, Halton City, TX 76148 (US).</p> <p>(74) Agents: PARMELEE, Steven, G. et al.; Motorola, Inc., Intellectual Property Dept., 1303 East Algonquin Road, Schaumburg, IL 60196 (US).</p>		<p>(81) Designated States: AT (European patent), BE (European patent), CA, CH (European patent), DE (European patent), DK (European patent), ES (European patent), FR (European patent), GB (European patent), GR (European patent), IT (European patent), JP, LU (European patent), NL (European patent), SE (European patent).</p> <p>Published <i>With international search report.</i></p>

(54) Title: METHOD AND APPARATUS TO SUSPEND SPEECH



(57) Abstract

A communication device (10) comprises an input for receiving a first audio signal (12) and an input (36) for receiving a second audio signal. The communication device (10) also includes a circular buffer (34) for storing the output digital audio signal (12) and for producing an output audio signal (20). The communication device (10) determines whether at least a portion of the output audio signal (20) is present in the second audio signal. If such a portion is present, the communication device (10) subtracts the portion of the output audio signal (20) from the second audio signal, to produce a third audio signal (26). A threshold detector, responsive to the third audio signal, produces a first control signal, when the level of the third audio signal increases to at least a first predetermined level, and a second control signal, when the level of the third audio signal decreases to a value lower than a second predetermined level, where the second predetermined level is lower than the first predetermined level. Finally, a buffer control (22) causes the buffer (34) to suspend production of the output audio signal in response to the first control signal, and causes the buffer (34) to resume production of the audio signal (12), in response to the second control signal.

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METHOD AND APPARATUS TO SUSPEND SPEECH**Technical Field**

15 This invention relates generally to communication systems and devices.

Background

20 A person listening to a radio in a noisy environment (e.g., an automobile) may miss some of the received speech when a noise occurs (e.g., when a horn honks, a car door is slammed or a person in the car speaks). In such cases the radio user may have to ask the person at the other end to repeat the message. In broadcast situations, such as where one dispatcher is talking to
25 many vehicles, requesting an interruption of the dispatcher is difficult. Moreover, in voice command communication systems, the person making a voice command may also miss portions of received audio while making voice commands.

30 Summary of the Invention

Briefly, according to the invention, a communication device comprises means for receiving a first audio signal and means for receiving a second audio signal. The communication device also includes a buffer for storing the first digital audio signal at an input
35 rate, and for producing an output audio signal at an output rate. The communication device determines whether at least a portion

of the output audio signal is present in the second audio signal. If such a portion is present, the communication device subtracts the portion of the output audio signal from the second audio signal, to produce a third audio signal. Threshold detection means, responsive to the third audio signal, produce a first control signal, when the level of the third audio signal increases to at least a first predetermined level, and a second control signal, when the level of the third audio signal decreases to a value lower than a second predetermined level, where the second predetermined level is lower than the first predetermined level. Finally, buffer control means cause the buffer to suspend production of the output audio signal in response to the first control signal, and cause the buffer to resume production of the audio signal, in response to the second control signal.

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Brief Description of the Drawings

Figure 1 is a simplified block diagram showing an audio-producing system in accordance with the invention.

Figure 2 is a flow chart illustrating a possible mode of operation for the communication device of the invention.

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Detailed Description of the Preferred Embodiment

Referring to Figure 1, a simplified block diagram is shown, depicting a communication device 10 in accordance with the invention. An audio (e.g., speech) input signal 12 is provided to an analog-to-digital converter 14, for producing a digital audio signal 16 at an input rate set by the buffer control logic 22. The digital audio signal 16 is then sampled and stored in a circular buffer 34 (well known in the art) that comprises a memory 18 (e.g., a large dynamic random access memory or DRAM) and buffer control logic 22. The buffer control logic 22 causes the memory 18 to sample and store the input audio signal 12 (at an input rate), and provides an input rate clock signal 21 to the analog-to-digital converter 14, and an output rate clock signal 23 to the digital-to-analog converter 44. The clock signals 21 and 23 set the input and output rates (respectively) of the device 10. Moreover, the

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buffer control logic 22 controls the memory address to cause successive samples to be stored in an increasing number of memory locations. The buffer control logic 22 additionally controls a similar process to read samples from the memory 18 to produce a digital audio signal 20, and convert it to a restored analog signal 40 with good fidelity, with a digital-to-analog converter 24, for presentation to a listener by a speaker 42. The rate at which the digital audio signals 20 are read from the memory 18 and converted to analog form, with the analog-to-digital converter 44, is controlled by the buffer control logic 22. In addition, the buffer control logic 22 can offset the read address from the write address of the audio stored in the memory 18, to produce a delay that is well known in the art.

The output process of the memory 18 can be stopped, thus resulting in a continual increase in the amount of speech 12 stored in the memory 18. In one embodiment of the invention, the output process may proceed at a rate identical to the input rate, resulting in a fixed delay. However, in another embodiment of the invention the output process may proceed at a higher rate than the input, thus gradually decreasing the total delay.

A high ambient signal detector 38 (a converse of a conventional speech detector) comprises a signal correlator and subtractor 24 and an adjustable threshold detector 28 with hysteresis. The inputs to the correlator and subtractor 24 are the digital audio signal 20 from the memory 18, and one or more ambient signal inputs 36, such as sounds (received by a microphone in a vehicle, for example) that are not produced by the communication device 10. The ambient signals received at the ambient signal inputs 36 are digitized by an analog-to-digital converter 25, before they are applied to the correlator and subtractor 24. The correlator and subtractor 24 can determine the delay from the speech output to the input devices, by cross-correlation, and then subtract at least a portion of the output signal 20 from the ambient signal inputs 36 to produce a signal 26 representing the net ambient noise level. The signal 26 is applied to the adjustable threshold detector 28 which provides a

"high ambient signal" control signal 30 (which is Boolean) when the signal 26 equals or exceeds a first predetermined threshold level (i.e., a "true" condition). When the signal 26 decreases below a second predetermined threshold level, a false condition occurs. The second predetermined threshold level, which is lower than the first predetermined threshold level, may be selected so that hysteresis is accomplished. The first and second threshold levels could be preset (e.g., as with potentiometers), or could be dynamically determined from the time-averaged ambient signal level. Signal correlators, subtractors, and threshold means are frequently performed with digital signal processors, well known in the art.

The "high ambient signal" 30 is applied to decision logic circuitry 32 which, in turn, controls the buffer control logic 22. A false-to-true transition will stop the output process; while a true-to-false transition will start the output process from the point at which it was stopped. The pulse extender logic 32 delays the true-to-false transition by an optional, adjustable amount (e.g., less than one second). Optionally, the output process can then proceed at a slightly higher rate than than the input rate, to gradually decrease the output delay toward zero, while unobjectionally increasing the frequency of the stored speech. According to another option, the pulse extender logic 32 can decrease the output delay by causing pauses in the speech input signal 16 to be reduced by detecting and skipping memory locations that have low signal power.

Referring to Figure 2, a flow chart 100 illustrates a possible mode of operation for the communication device 10. Under decision 102, if no input audio signal 12 is present, the circular buffer 34 is set to zero delay (104). Under decision 106, when the audio input signal 12 is received, that signal 12 is passed (108) through the circular buffer 34 without delay to the speaker 42. The speaker signal is received by the ambient sound inputs 36 but will be substantially removed, and will not produce a "high ambient signal" decision. Also under decision 106, if an external source produces a sound that is received by the ambient sound

inputs 36 which is not cancelled by the output audio signal 20, the output process is stopped (110). However, the input process continues, continually increasing the effective length of the output delay.

5 When the ambient sound stops (112), the "high ambient signal" decision becomes "false." After a brief delay, to allow the user to differentiate between the received signal from the ambient signal, the output process will resume (114) at the point where it left off at the time of interruption. The input and output processes
10 will continue at substantially identical rates, with input and output signals delayed by the offset time (which is equal to the interruption time). Thus if a dispatcher is being heard, the delayed replica of the dispatcher's speech will be heard as if the dispatcher had paused during the interrupting sounds. The delay
15 time remains constant until the received signal terminates and the delay line is flushed. At that time the circular buffer 34 is reset to zero delay.

 According to another embodiment of the invention, if the communication device 10 is voice operated or there is another
20 voice operated device near the communication device 10, the ambient signal inputs could be set to receive the voice commands and the audio output process could be interrupted by the voice commands in the manner described above. Therefore, the invention provides a way to preserve an entire message received
25 by the communication device 10 even when ambient sounds or voice commands interrupt reception.

What is claimed is:

Claims

1. A communication device, comprising:
- 5 means for receiving a first audio signal;
means for receiving a second audio signal;
a buffer for storing the first audio signal at an input rate,
and for producing an output audio signal at an output rate;
determining means for determining whether at least a
10 portion of the output audio signal is present in the second audio
signal;
subtracting means for subtracting the portion of the output
audio signal from the second audio signal, to produce a third
audio signal;
15 threshold detection means responsive to the third audio
signal, for producing a first control signal when the third audio
signal increases at least to a first predetermined level, and for
producing a second control signal when the third audio signal
decreases below a second predetermined level, the second
20 predetermined level being lower than the first predetermined
level;
and
buffer control means for causing the buffer to suspend
production of the output audio signal in response to the first
control signal, and for causing the buffer to resume production of
25 the audio signal, in response to the second control signal.

2. The communication device of claim 1, further comprising:

an analog-to-digital converter for converting the first audio signal into digital form before the first audio signal is stored in the buffer;

a digital-to-analog converter for converting the output audio signal to analog form, and

an analog-to-digital converter for converting the second audio signal into digital form.

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3. The communication device of claim 1, wherein the input rate is equal to the output rate.

4. The communication device of claim 1, wherein the output rate is greater than the input rate.

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5. The communication device of claim 1, wherein the buffer control means controls the output rate of the buffer.

6. The communication device of claim 1, wherein the second audio signal is noise produced by a source different from the source of the first audio signal.

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7. The communication device of claim 1, wherein the second audio signal is a voice command produced by a source different from the source of the first audio signal.

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8. The communication device of claim 1, wherein the communication device is a radio.

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9. The communication device of claim 1, wherein the determining means comprises a correlator.

10. The communication device of claim 4, wherein the buffer control means locates pauses in the first audio signal and removes the pauses during production of the output audio signal.

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11. The communication device of claim 5, wherein the buffer control means causes the output rate to be greater than the input rate.

12. A method for suspending the production of an audio signal, comprising the steps of:
- receiving a first audio signal;
 - receiving a second audio signal;
 - 5 storing the first digital audio signal in a buffer, at an input rate;
 - producing an output audio signal at an output rate;
 - removing at least a portion of any of the output audio signal present in the second audio signal, to produce a third audio
 - 10 signal;
 - producing a first control signal when the third audio signal increases at least to a first predetermined level;
 - producing a second control signal when the third audio signal decreases below a second predetermined level, the
 - 15 second predetermined level being lower than the first predetermined level;
 - causing the buffer to suspend production of the output audio signal in response to the first control signal; and
 - causing the buffer to resume production of the audio
 - 20 signal, in response to the second control signal.

13 The method of claim 12, further comprising the steps of:
converting the first audio signal into digital form; and
converting the second audio signal into digital form.

5 14. The method of claim 12, further comprising the step of
causing the input rate to be equal to the output rate.

15 15. The method of claim 12, further comprising the step of
causing the output rate to be greater than the input rate.

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16. The method of claim 12, wherein the second audio
signal is noise produced by a source different from the source of
the first audio signal.

15 17. The method of claim 12, wherein the second audio
signal is a voice command produced by a source different from
the source of the first audio signal.

20 18. The method of claim 12, further comprising the step of
locating pauses in the first audio signal and removing the pauses
during production of the output audio signal.

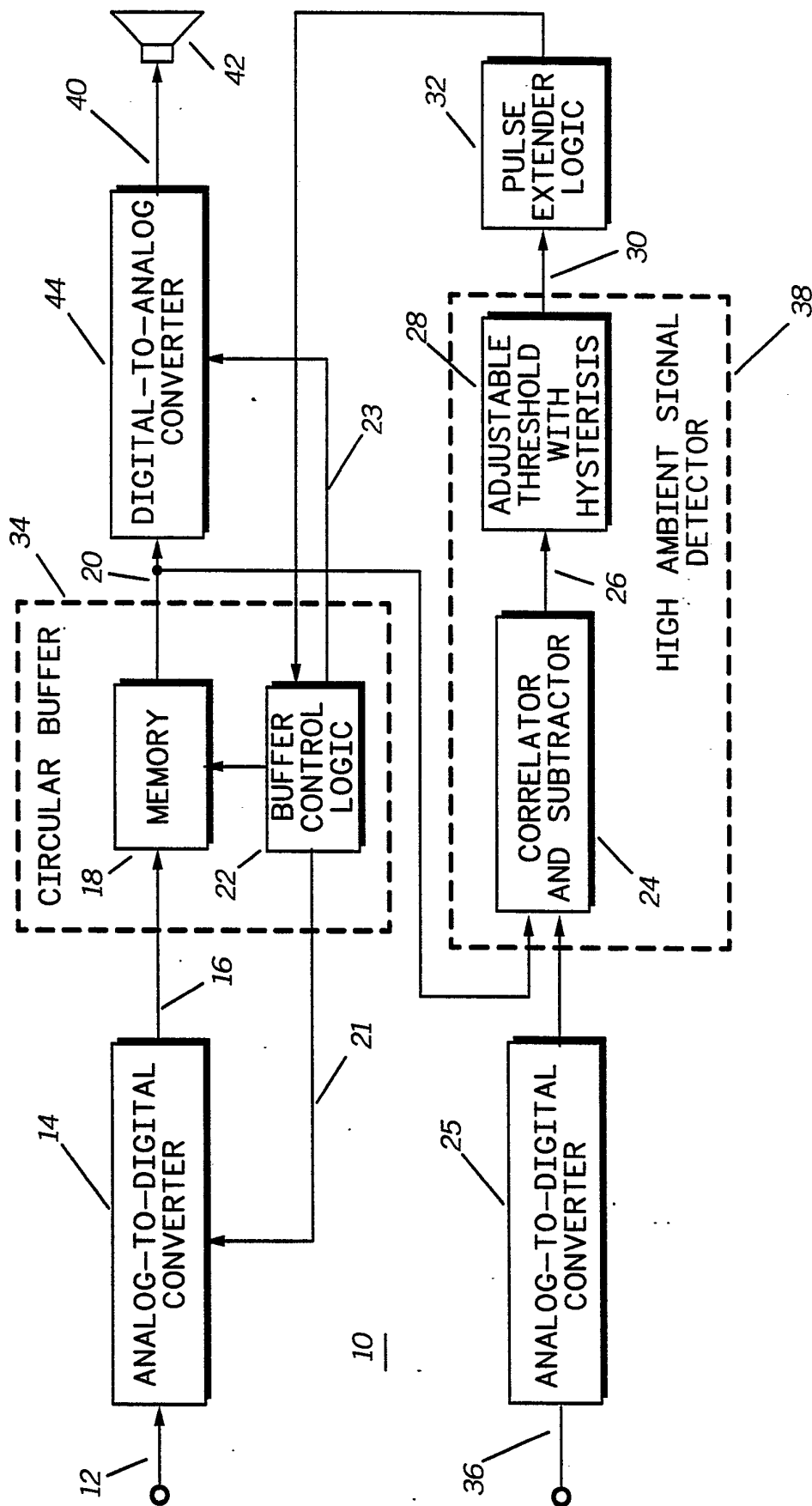


FIG. 1

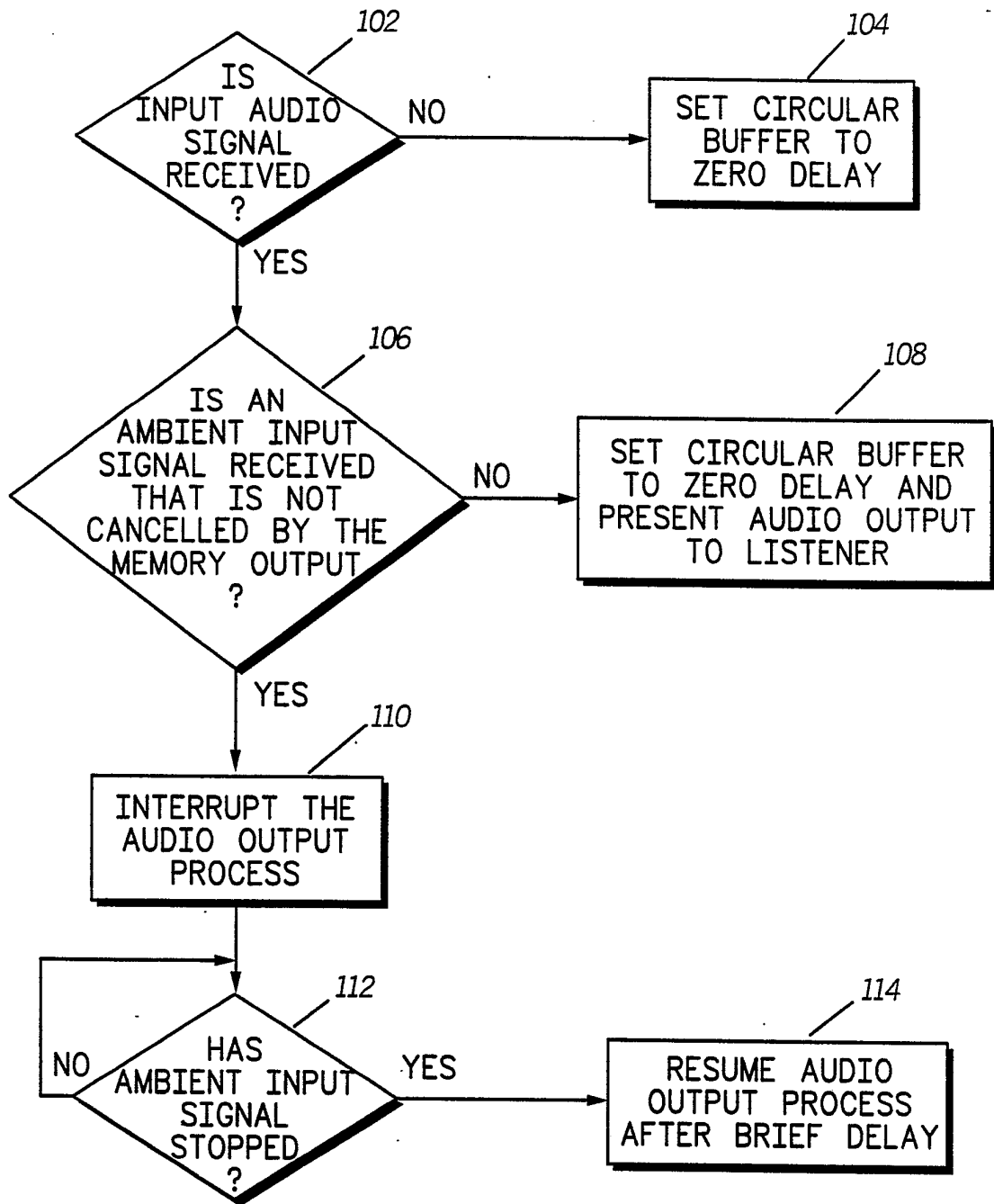
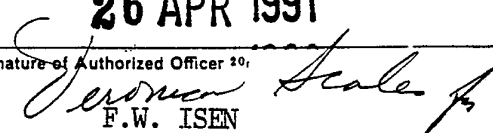


FIG. 2

INTERNATIONAL SEARCH REPORT

International Application No

PCT/US91/00873

I. CLASSIFICATION OF SUBJECT MATTER (if several classification symbols apply, indicate all) ³		
According to International Patent Classification (IPC) or to both National Classification and IPC		
IPC(5): H03G 3/20		
US CL.: 381/110		
II. FIELDS SEARCHED		
Minimum Documentation Searched ⁴		
Classification System	Classification Symbols	
US	381/94,110,57,107; 379/57,58,59,63; 455/296,297,238	
Documentation Searched other than Minimum Documentation to the Extent that such Documents are Included in the Fields Searched ⁵		
III. DOCUMENTS CONSIDERED TO BE RELEVANT ¹⁴		
Category [*]	Citation of Document, ¹⁵ with indication, where appropriate, of the relevant passages ¹⁷	Relevant to Claim No. ¹⁸
A	US, A, 4,468,813 (BURKE et al.) 28 August 1984	
A	US, A, 4,677,389 (OP DE DEEK et al.) 30 June 1987	
A	US, A, 4,811,382 (SLEEVI) 07 March 1989	
A	US, A, 4,827,458 (D'ALAYER DE COSTEMORE D'ARC) 02 May 1989	
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IV. CERTIFICATION		
Date of the Actual Completion of the International Search ²	Date of Mailing of this International Search Report ²	
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