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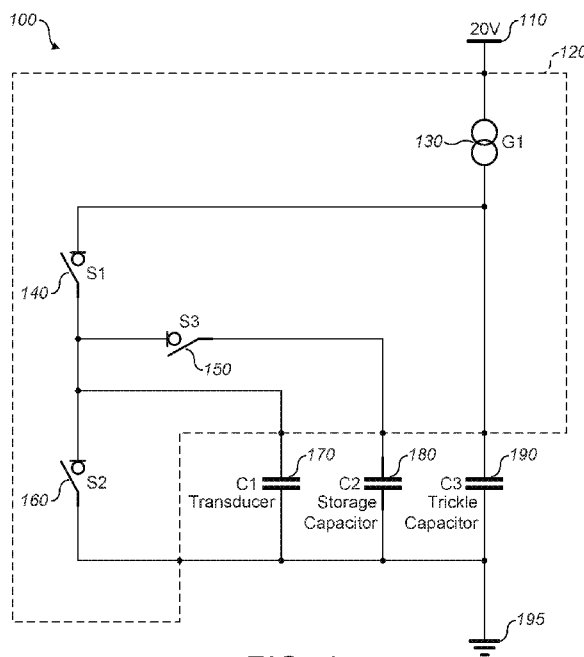


FIG. 1

(57) Abstract: A circuit for driving a transducer in a mid-air haptic system includes a voltage source, a voltage sink, a current source, a trickle capacitor, a storage capacitor, a haptic system transducer, a first switch, a second switch, and a third switch. Using these components, a portion of the charge required for switching a transducer is sourced from the decoupling capacitance. When the switching completes, additional charge is transferred immediately from the power supply back into the decoupling capacitance. This acts to lower the peak current by fully utilizing 100% of a switching waveform for transfer of charge from the power supply to capacitors local to the transducer.

TRANSDUCER DRIVER

RELATED APPLICATION

[0001] This application claims the benefit of the following U.S. Provisional Patent Application, which is incorporated by reference in its entirety:

[0002] 1) Serial No. 62/433,785, filed on December 23, 2016.

FIELD OF THE DISCLOSURE

[0003] The present disclosure relates generally to improved driving techniques for acoustic transducers in mid-air haptic-based systems.

BACKGROUND

[0004] A continuous distribution of sound energy, referred to as an “acoustic field”, may be used for a range of applications including parametric audio and the levitation of objects.

[0005] By defining one or more control points in space, the acoustic field can be controlled. Each point may be assigned a value equating to a desired amplitude at the control point. An acoustic field exhibiting the desired amplitude at the control points is created by actuating the set of transducers as a phased array. Focusing the energy in the desired control point location implies the transducers are excited at different times such that the waves output from each arrive together.

[0006] Specifically, mid-air haptic feedback is generated with array(s) of ultrasonic transducers. Typical arrays contain 256 (or more) transducers, all driven independently, to generate ‘focal points’ through constructive and destructive interference of the audio output from each transducer. To simplify implementation of the signal processing used to stimulate the ultrasonic transducers the fixed frequency digital inputs (often 40kHz) are typically divided

into a fixed number of phases, with each transducer switching synchronously on a phase transition.

[0007] Ultrasonic transducers are typically driven with a digital electrical input signal, generating an analog audio output signal. The digital signal is typically originally generated at 3V (or less) by digital signal processing circuits but the ultrasonic transducer must be driven with a higher voltage, typically 20V, to maximize the audio power emitted by the transducer. When the digital control signal switches to 3V the transducer will be driven to 20V. When the digital control signal switches to 0V the transducer will also be driven to 0V. The ultrasonic transducers are typically driven at a fixed frequency (often 40kHz) corresponding to the resonant frequency of the transducer.

[0008] Existing driver circuits can provide direct voltage drive to the transducer, driving the capacitive load to the 20V output voltage through a small (ideally zero) output impedance. Alternatively they can provide current drive, driving the capacitive load to the 20V output voltage by sourcing a significant (typically 100's of mA) current.

[0009] The digital control signal is typically 'level shifted' from the 3V low voltage to the 20V high voltage with the use of a level shifting circuit. Since the transducer has a significant capacitance, typically 2nF (or more) then the level shifting circuit must be capable of sourcing (sinking) a significant current for a very short time when the transducer switches between 0V and 20V.

[0010] Typical peak switching current for a single transducer with input capacitance of 2nF switching between 0V and 20V in 100ns would be given by

$$i = C \frac{dV}{dt} = 2nF * \frac{20V}{100ns} = 2 * \frac{1}{5} = 0.4A = 400mA$$

Local 'decoupling' capacitance is typically used to reduce the demand for current from a power supply during this switching, often reducing the peak current to about 200mA over 200ns.

[0011] With an array of (typically) 256 transducers it will be common for several transducers to switch (between 0V and 20V) at the same time. For example, if 8

transducers switch simultaneously then the peak current required from the power supply would be $8 \times 400\text{mA}$ or 3.2A without local decoupling capacitance and 1.6A with local decoupling capacitance, corresponding to eight driver circuits each requiring peak current of 400mA .

[0012] The peak current in this example would last for 200ns but if the 40kHz digital input frequency was divided in (say) 64 phases then each phase would last for 390ns and the power supply would need to source an average of approximately 0.5A , with the peak being 1.6A .

[0013] Switching the load on a power supply between 0A and $>1\text{A}$ every 200ns can cause major variation on the output voltage of the power supply based on the load transient response. Depending on the power supply characteristics this could cause more than 10% variation (for example) in the nominal 20V output voltage so that actual power supply output voltage is somewhere between 18V and 22V , leading to both an uncontrolled reduction and increase in output audio power and cross modulation between transducers impacting the control point(s).

[0014] The example described above requires an average of 0.5A with a peak of 1.6A for 8 transducers switching at the same time. If 16 or 32 transducers were to switch at the same time the average (peak) current would become 1A (3.2A) and 2A (6.4A) respectively.

[0015] Typical power supplies have a peak output current they can support. Whatever the peak output current for the power supply, with an array of ultrasonic transducers driven as described there will be an upper limit on the number of transducers that may switch at the same time.

[0016] Accordingly, there is a need for an improved transducer circuit that can address the foregoing limitations and drive the transducers in a more efficient manner.

SUMMARY OF THE INVENTION

[0017] By adding local decoupling capacitance combined with a method of constant current charging of the local decoupling capacitance, the PWM switching

of the transducer can be isolated from the accumulation of the energy needed to switch the transducer minimizing the average current drawn from the power supply and significantly reducing the peak current requirements for the power supply. Additionally the charge required to switch the transducer can be locally reused to further reduce the average current drawn from the power supply.

BRIEF DESCRIPTION OF THE FIGURES

[0018] The accompanying figures, where like reference numerals refer to identical or functionally similar elements throughout the separate views, together with the detailed description below, are incorporated in and form part of the specification, and serve to further illustrate embodiments of concepts that include the claimed invention, and explain various principles and advantages of those embodiments.

[0019] Figure 1 shows a partial integrated circuit-based solution for a driver for a single transducer.

[0020] Figure 2 shows a partial integrated circuit-based solution for a driver for multiple transducers.

[0021] Figure 3 shows a printed circuit board-based solution for a driver for a single transducer.

[0022] Skilled artisans will appreciate that elements in the figures are illustrated for simplicity and clarity and have not necessarily been drawn to scale. For example, the dimensions of some of the elements in the figures may be exaggerated relative to other elements to help to improve understanding of embodiments of the present invention.

[0023] The apparatus and method components have been represented where appropriate by conventional symbols in the drawings, showing only those specific details that are pertinent to understanding the embodiments of the present invention so as not to obscure the disclosure with details that will be readily apparent to those of ordinary skill in the art having the benefit of the description herein.

DETAILED DESCRIPTION

[0024] I. Problems to be Addressed

[0025] A solution for an improved transducer driver should be designed to address at least the following problems.

[0026] Problem 1: Peak current required from the power supply is excessive.

[0027] For representative array of 256 transducers described above this will be greater than 1A.

[0028] Problem 2: Power supply current varies with number of simultaneously switching transducers.

[0029] For the representative array of 256 transducers described above the required current can reach 4A and realistically exceeds the peak output current of any practical consumer power supply and introduces distortion into the audio output, impacting the implementation of the control point.

[0030] Problem 3: Direct drive of transducers from the high voltage power supply

[0031] Any driver circuit which connects the transducer directly to the 20V high voltage power supply has an inherent failing that there can be no power supply rejection and the power supply load transient response will drive – not couple, drive – directly into all transducers, introducing crosstalk between transducer outputs.

[0032] Problem 4: Unwanted modulation of the transducer outputs

[0033] Since existing solutions switch large currents (problem 1) with the instantaneous current significantly varying based on numbers of switching transducers (problem 2) then the direct driving of transducers from the power supply (problem 3) creates an additional problem – the load transient response of the power supply directly modulates all active transducers in the array, further degrading the implementation of the control point.

[0034] Problem 5: Discharging transducer (charge) into ground is inefficient.

[0035] Charge removed from the transducer when switching from 20V to 0V is discarded by existing solutions by transferring it to ground, which makes existing approaches inefficient.

[0036] II. Introduction to the Solution

[0037] The circuit requirement is for the transducer to be driven to 20V by charging its 2nF input capacitance. The prior solution acts to transfer the charge required to switch the transducer (to 20V) from the power supply over the same time the transducer is switching, leading to huge peak currents for very short periods of time, with no current required for the majority of the 40kHz cycle.

[0038] Additionally, during the period that the transducer is held at 20V this is achieved with a direct connection between the 20V power rail and the transducer. Any variation on the 20V power rail directly drives (not couples, drives) onto the transducer, modulating the audio output.

[0039] If local “decoupling” capacitance is included with the level shifter circuit then part of the charge required for switching is sourced from the decoupling capacitance. Thus when switching completes, additional charge is transferred immediately from the power supply back into the decoupling capacitance. This acts to lower the peak current but extends the time current is required.

[0040] The prior solution does not utilize the full period of the 40kHz digital switching signal. If the 40kHz switching signal is divided into 64 equal phases, each phase corresponds to 390ns. The period of a 40kHz frequency is 25,000ns. Assuming the switching time for a transducer is 100ns then every 25,000ns the transducer will switch once from 0V to 20V and from 20V to 0V.

[0041] The transition from 0V to 20V deposits charge from the power supply onto the transducer. The transition from 20V to 0V removes charge from the transducer into ground.

[0042] In this representative example only 0.4% (100ns in 25,000ns) of the 40kHz digital switching waveform corresponds to transfer of charge onto the transducer to drive its voltage to 20V. The remaining 99.6% of the 40kHz switching waveform is unused for transfer of charge from the power supply with the existing

approach. The 99.6% of unused time could be used for the transfer of charge from the power supply into a local storage capacitor. When charge is required to switch the transducer from 0V to 20V it can be supplied from this local storage capacitor.

[0043] The present invention solves problem 1 by fully utilizing 100% of the 40kHz switching waveform for uniform transfer of charge from the power supply to capacitance local to the transducer.

[0044] By using 100% of the 40kHz cycle for transfer of charge from the power supply the peak current required from the power supply can be reduced by a factor of $1/0.004 = 250$ (from 0.4%) so the current per transducer decreases from a peak of 400mA centered at the PWM switching instant to a constant $400\text{mA}/250 = 1.6\text{mA}$ spread across the entire 40kHz cycle.

[0045] The solution relies on ‘trickle charging’ a local storage capacitor and using this local charge to switch the transducer to 20V such that the power supply loading remains (almost) constant throughout the 40kHz cycle. This approach enables any number of transducers, unrestricted by power supply performance, to switch simultaneously, since the load on the power supply is almost constant.

[0046] Since the charge required to switch each transducer (to 20V) is accumulated over the full 40kHz cycle then the two actions of collecting the charge needed for switching and the actual switching are separated, meaning that the current drawn from the power supply becomes independent to the number of transducers switching. The present invention thus removes the practical limit on the number of concurrently switching transducers.

[0047] The concept described herein does not drive the transducer directly from the power supply, rather the power supply is used to accumulate charge on a local capacitor which is then used to charge the transducer to switch from 0V to 20V. Addition of some form of voltage detection and limiting to the transducer relaxes the requirement for an accurate stable power supply. So long as the local storage capacitor accumulates sufficient charge during the 40kHz cycle the transducer will charge to the required controlled 20V voltage. The additional limiting

function can be achieved in many different ways but one possible approach for a discrete PCB implementation would be the use of a 20V Zener diode connected in parallel to the transducer.

[0048] The concepts described below reduce the current draw requirements of a 256 transducer array from several A to a few hundred mA. To reduce current drain further requires that charge used to switch the transducer to 20V is removed from the transducer, but critically not discarded, when switching to 0V. When switching the transducer back to 20V this local retained charge can be deposited back on the transducer to switch the transducer partially towards the 20V voltage. This directly reduces the charge which must be transferred from the main (local) storage capacitor. In this way, the main capacitor requires less charge in each 40kHz cycle to return to the fully charged condition.

[0049] III. Trickle Charging

[0050] Typical ultrasonic transducers act as a 2.5nF capacitive load (on the driver circuit) and must be driven with 15-20V, with the transducer voltage switching between 0V and 20V in typically 100ns.

[0051] Existing solutions switch transducers between 0V and 20V driving directly from the power supply.

[0052] Switching 2nF within 100ns to 20V requires an average current (into the transducer) of 400mA.

$$i = C \frac{dV}{dt} = 2nF * \frac{20V}{100ns} = 2 * \frac{1}{5} = 0.4A = 400mA$$

For a typical implementation using an array of 256 transducers where 10% switch synchronously the peak current required from the power supply would be approximately $256 * \frac{10}{100} * 400mA = 10.2A$ for 100ns then return to 0A, the effective load per transducer varying abruptly between (approx.) 40Ω and open circuit.

[0053] For a situation where transducers are switched at their resonant frequency (typically 40kHz) with a 64 phase PWM waveform then the power supply is

presented with this load at a 2.56MHz rate since switching instants occur every 390ns.

[0054] Reducing peak current per transducer (in a 64 phase solution) by increasing switching time by 3.9x degrades ultrasonic performance but still requires a power supply capable of delivering 2.6A.

[0055] If the number of switching phases within the 40kHz PWM cycle increases then the required switching time to switch each transducer from 0V to 20V decreases and the peak current drawn from the power supply increases.

[0056] This approach transfers charge from the power supply for (approx.) 100ns every 25,000ns and charge transfer only makes effective use of 4% of the 40kHz switching period.

[0057] Addition of a local capacitor per transducer enables a more efficient use of the 40kHz switching period. By trickle charging this local capacitor throughout the full 40kHz cycle, the effective load on the power supply (per transducer) is translated into an almost constant (approx.) 10kΩ load.

[0058] When the transducer switches from 0V to 20V charge is transferred onto the transducer capacitance from the local storage capacitor rather than directly from the power supply.

[0059] An array of 256 transducers operating with the trickle charging technique requires an almost constant current of 384mA and can support all transducers simultaneously switching, providing an improvement of $\frac{2.5nF * \frac{20V}{100ns} * \frac{10}{100}}{0.384A}$ or 33x over existing approaches for an array of 256 transducers with 256 phases with 10% coincident switching.

[0060] With a typical 20V power supply capable of sourcing 5A, the existing technique for switching transducers would be unable to switch more than 10 transducers switching simultaneously. Trickle charging local storage capacitance reduces peak current draw from >10,000mA to 384mA, a performance improvement of 26x.

[0061] IV. Voltage Clamping at Transducer

[0062] Typical ultrasonic transducers have an input capacitance of approximately 2nF but with a typical tolerance of $\pm 20\%$.

[0063] Existing solutions drive each transducer directly from a common power supply. Often this power supply is 20V, driving transducers to the maximum rated voltage. By driving all transducers directly from the power supply the output voltage for all transducers (after initial switching completes) will be identical and match power supply voltage (often 20V).

[0064] Adopting an indirect charging approach results in the transducer being current driven rather than voltage driven. The output voltage on each transducer when current driven depends on the specific capacitance of each individual transducer. Additionally, when charge sharing is used to drive the transducer voltage the specific value of the local charge storage capacitor affects the transducer voltage.

[0065] Monitoring the output voltage for each transducer and stopping current drive / charge sharing when the transducer voltage reaches a fixed voltage (common for all transducers in the array) acts to eliminate variations in transducer voltage caused by variations in either the transducer capacitance or variations in the local charge storage capacitance.

[0066] V. Charge Recirculation onto/from Transducer

[0067] The existing approach for directly driving ultrasonic transducers, described above, sources charge from VDD (or voltage source) to drive the transducer to 20V and discharges the transducer into VSS (or ground or voltage sink) to drive the output to 0V, generating a PWM output waveform. This sequence is repeated at the resonant frequency of the transducer, often 40kHz.

[0068] Charge pumped onto the transducer every 40kHz cycle is also discarded during each 40kHz cycle.

[0069] Transfer of charge away from the transducer onto a local storage capacitor when the transducer must switch from 20V to 0V and subsequent transfer of the locally stored charge back onto the transducer when switching the transducer from

0V to 20V would minimize the amount of charge discarded during each 40kHz cycle.

[0070] One possible implementation of this technique would add one capacitor and one switch to the transducer driver circuit. The switch could be used to transfer charge away from the transducer onto the local storage capacitor during the transition 20V to 0V. The switch can also be used to transfer charge back onto the transducer during the transition from 0V to 20V. In such an implementation the amount of charge transferred will depend on the capacitance ratio between the transducer and the local storage capacitor. Any excess charge remaining during the 20V to 0V transition would still be discharged to VSS (or ground or voltage sink). Any additional charge required during the 0V to 20V transition would still be sourced from VDD (or voltage source).

[0071] This technique can offer 50% efficiency improvements to the current consumption of the transducer driver circuit described above

[0072] VI. Amplitude Modulation of Transducer Output

[0073] By limiting the transfer of charge from the trickle and storage capacitors into the transducer, the amplitude of the two state, digital electrical input voltage applied to the transducer can be controlled resulting in control of the amplitude of the analog audio output signal from the transducer.

[0074] Controlling the voltage used to drive the transducer reduces the charge required to drive the transducer to the target voltage, which lowers the average current drawn from the power supply to operate each transducer. In this way the power consumption of the transducer array can be further reduced.

[0075] VII. Figure Descriptions

[0076] Turning to Figure 1, shown is a partial integrated circuit-based solution for a driver for a single transducer 100. The integrated circuit components are located within the dashed border 120; discrete components are located outside the dashed border 120. The integrated circuit is connected to a 20V (or other voltage) power supply 110 and to ground 195 that is intended to provide the necessary voltage for the transducer 170. Within the integrated circuit, a current source G1 130 drives

current to constantly charge the trickle capacitor C3 190. A first switch S1 140 is installed between the current source 130 and two connections, one to the transducer C1 170 and one to the storage capacitor C2 180. A second switch S2 160 is installed between first switch S1 140 and in parallel with a transducer C1 170. A third switch S3 150 is installed between the first switch S1 140 and a storage capacitor C2 180.

[0077] Switch S1 140 is used to charge the transducer from 0V to 20V. When switch S1 140 is closed charge accumulated on the capacitor C3 190 is transferred onto the transducer C1 170. Current from the current source G1 130 is also provided to both capacitor C3 190 and transducer C1 170. Flow of charge between capacitor C3 190 and transducer C1 170 will cease when the same voltage is developed across both components. Switch S1 140 will only close when the transducer C1 170 is at 0V when driven to create haptic feedback.

[0078] When switch S1 140 is open the current from the current source G1 130 flows into the capacitor C3 190, charging the capacitor towards 20V. As the voltage across capacitor C1 170 approaches 20V the current source G1 130 will begin to collapse.

[0079] Switch S2 160 is used to discharge the transducer C1 170 from 20V to 0V. When switch S2 160 is closed the charge on the transducer C1 170 is discharged into VSS or ground 195.

[0080] When switch S2 160 is open the transducer C1 170 is prevented from discharging into VSS or ground 195.

[0081] When switch S3 150 is closed the local storage capacitor C2 180 is connected into the circuit. Switch S3 150 is only closed when both switch S1 190 and switch S2 160 are open.

[0082] When switch S3 150 is closed and the transducer C1 170 voltage is 20V then the charge stored on the transducer C1 170 is transferred to local storage capacitor C2 180. Flow of charge between transducer C1 170 and local storage capacitor C2 180 will cease when the same voltage is developed across both components.

[0083] When switch S3 150 is closed and the transducer C1 170 voltage is 0V then the charge stored on the local storage capacitor C2 180 is transferred to transducer C1 170. Flow of charge between transducer C1 170 and local storage capacitor C2 180 will cease when the same voltage is developed across both components.

[0084] When discharging the transducer C1 170 into the local storage capacitor C2 180 there will be a residual charge retained on the transducer C1 170 requiring switch S3 150 to be opened and switch S2 160 to be closed to fully discharge transducer C1 170 to 0V.

[0085] When charging the transducer C1 170 from the local storage capacitor C2 180 the voltage on transducer C1 170 may not reach 20V depending on the available charge on local storage capacitor C2 180. Switch S3 150 will be opened and switch S1 140 closed to transfer the required charge from capacitor C3 190 to charge transducer C1 170 to 20V.

[0086] When switch S3 150 is open the local storage capacitor C2 180 is isolated to retain charge needed for subsequent charging of transducer C1 170 to 20V.

[0087] Capacitors C2 180 and C3 190 may typically be external components mounted on a printed circuit board but there is no functional reason they could not be integrated onto a silicon chip.

[0088] Capacitor C3 190 and current source G1 130 isolate switching of the transducer C1 170 from the 20V power supply 110. Switches S1 140, S2 150 and S3 160 are used for the rapid transfer of charge between transducer C1 170, local storage capacitor C2 180 and trickle capacitor C3 190. The on resistance of switches S1 140, S2 150 and S3 160 must be minimized towards a few Ohms to maintain efficiency of the switching activity as charge is moved between transducer C1 170, local storage capacitor C2 180 and trickle capacitor C3 190. Implementation of low on-resistance switches in integrated circuit form requires the use of very large transistors and switches S1 140, S2 150 and S3 160, which may constitute the majority of the silicon die area for the circuit shown in Figure 1 100.

[0089] Turning to Figure 2, shown is a partial integrated circuit-based solution for a driver for multiple transducers 200. The integrated circuit components are located within the dashed border 204; discrete components are located outside the dashed border 204. The integrated circuit is connected to a 20V (or other voltage) power supply 202 and to ground 206a, 206b, 206c, 206d, 206e that is intended to provide the necessary voltage for the four shown transducers C3 220a, 220b, 220c, 220d. (Although 4 transducers are shown in Figure 2, any two or more transducers may be arranged in the same way.) Within the integrated circuit, a current source G1 240 drives current to constantly charge the trickle capacitor C9 242.

[0090] Within the integrated circuit, a current source G1 130 drives current to constantly charge the trickle capacitor C3 190. A first switch S1 140 is installed between the current source 130 and two connections, one to the transducer C1 170 and one to the storage capacitor C2 180. A second switch S2 160 is installed between first switch S1 140 and in parallel with a transducer C1 170. A third switch S3 150 is installed between the first switch S1 140 and a storage capacitor C2 180.

[0091] Each transducer C1 220a, C3 220b, C5 220c, C7 220d is wired in parallel with a corresponding storage capacitor C2 222a, C4 222b, C6 222c, C8 222d.

[0092] For each transducer, a first switch S1 208a, 208b, 208c, 208d is installed between the current source 240 and two connections, one to the transducer C1 220a, C3 220b, C5 220c, C7 220d and one to the storage capacitor C2 222a, C4 222b, C6 222c, C8 222d. A second switch S2 210a, 210b, 210c, 210d is installed between first switch S1 208a, 208b, 208c, 208d and in parallel with a transducer C1 220a, C3 220b, C5 220c, C7 220d. A third switch S3 212a, 212b, 212c, 212d is installed between the first switch S1 208a, 208b, 208c, 208d and a storage capacitor storage capacitor C2 222a, C4 222b, C6 222c, C8 222d.

[0093] Switch S1 208a, 208b, 208c, 208d is used to charge the transducer from 0V to 20V. When switch S1 208a, 208b, 208c, 208d is closed charge accumulated on the capacitor C3 242 is transferred onto the transducer C1 220a,

220b, 220c, 220d. Current from the current source G1 240 is also provided to both capacitor C3 242 and transducer C1 220a, 220b, 220c, 220d. Flow of charge between capacitor C3 242 and transducer C1 220a, 220b, 220c, 220d will cease when the same voltage is developed across both components. Switch S1 208a, 208b, 208c, 208d would only close when the transducer C1 220a, 220b, 220c, 220d is at 0V when driven to create haptic feedback.

[0094] When switch S1 208a, 208b, 208c, 208d is open the current from the current source G1 240 flows into the capacitor C3 242, charging the capacitor towards 20V. As the voltage across capacitor C1 220a, 220b, 220c, 220d approaches 20V the current source G1 240 will begin to collapse.

[0095] Switch S2 210a, 210b, 210c, 210d is used to discharge the transducer C1 220a, 220b, 220c, 220d from 20V to 0V. When switch S2 210a, 210b, 210c, 210d is closed the charge on the transducer C1 220a, 220b, 220c, 220d is discharged into VSS or ground 206a, 206b, 206c, 206d.

[0096] When switch S2 210a, 210b, 210c, 210d is open the transducer C1 220a, 220b, 220c, 220d is prevented from discharging into VSS or ground 206a, 206b, 206c, 206d.

[0097] When switch S3 212a, 212b, 212c, 212d is closed the local storage capacitor C2 180 is connected into the circuit. Switch S3 212a, 212b, 212c, 212d is only closed when both switch S1 208a, 208b, 208c, 208d and switch S2 210a, 210b, 210c, 210d are open.

[0098] When switch S3 212a, 212b, 212c, 212d is closed and the transducer C1 220a, 220b, 220c, 220d voltage is 20V then the charge stored on the transducer C1 220a, 220b, 220c, 220d is transferred to local storage capacitor C2 180. Flow of charge between transducer C1 220a, 220b, 220c, 220d and local storage capacitor C2 222a, 222b, 222c, 222d will cease when the same voltage is developed across both components.

[0099] When switch S3 212a, 212b, 212c, 212d is closed and the transducer C1 220a, 220b, 220c, 220d voltage is 0V then the charge stored on the local storage capacitor C2 222a, 222b, 222c, 222d is transferred to transducer C1 220a, 220b,

220c, 220d. Flow of charge between transducer C1 220a, 220b, 220c, 220d and local storage capacitor C2 222a, 222b, 222c, 222d will cease when the same voltage is developed across both components.

[00100] When discharging the transducer C1 220a, 220b, 220c, 220d into the local storage capacitor C2 222a, 222b, 222c, 222d there will be a residual charge retained on the transducer C1 220a, 220b, 220c, 220d requiring switch S3 212a, 212b, 212c, 212d to be opened and switch S2 210a, 210b, 210c, 210d to be closed to fully discharge transducer C1 220a, 220b, 220c, 220d to 0V.

[00101] When charging the transducer C1 220a, 220b, 220c, 220d from the local storage capacitor C2 222a, 222b, 222c, 222d the voltage on transducer C1 220a, 220b, 220c, 220d may not reach 20V depending on the available charge on local storage capacitor C2 222a, 222b, 222c, 222d. Switch S3 212a, 212b, 212c, 212d will be opened and switch S1 208a, 208b, 208c, 208d closed to transfer the required charge from capacitor C3 242 to charge transducer C1 220a, 220b, 220c, 220d to 20V.

[00102] When switch S3 212a, 212b, 212c, 212d is open the local storage capacitor C2 222a, 222b, 222c, 222d is isolated to retain charge needed for subsequent charging of transducer C1 220a, 220b, 220c, 220d to 20V.

[00103] Capacitors C2 222a, 222b, 222c, 222d and C3 242 would typically be external components mounted on a printed circuit board but there is no functional reason they could not be integrated onto a silicon chip.

[00104] Capacitor C3 242 and current source G1 240 isolate switching of the transducer C1 220a, 220b, 220c, 220d from the 20V power supply 202. Switches S1 208a, 208b, 208c, 208d, S2 210a, 210b, 210c, 210d and S3 212a, 212b, 212c, 212d are used for the rapid transfer of charge between transducer C1 220a, 220b, 220c, 220d, local storage capacitor C2 222a, 222b, 222c, 222d and trickle capacitor C3 242. The on resistance of switches S1 208a, 208b, 208c, 208d, S2 210a, 210b, 210c, 210d and S3 212a, 212b, 212c, 212d must be minimized towards a few Ohms to maintain efficiency of the switching activity as charge is moved between transducer C1 220a, 220b, 220c, 220d, local storage capacitor C2

222a, 222b, 222c, 222d and trickle capacitor C3 242. Implementation of low on-resistance switches in integrated circuit form requires the use of very large transistors and switches S1 208a, 208b, 208c, 208d, S2 210a, 210b, 210c, 210d and S3 212a, 212b, 212c, 212d constitute the majority of the silicon die area for the circuit shown in Figure 2 200.

[00105] The switches S1 208a, 208b, 208c, 208d, S2 210a, 210b, 210c, 210d and S3 212a, 212b, 212c, 212d in conjunction with local storage capacitor C2 222a, 222b, 222c, 222d comprise the driver circuit for transducer C1 220a, 220b, 220c, 220d. Trickle capacitor C3 242 and current source G1 240 could be replicated for each transducer driver circuit or could be a common resource shared between some or all of the driver circuits to optimize the number of external components on the printed circuit board.

[00106] Turning to Figure 3, shown is a printed circuit board-based solution for a driver for a single transducer 300. The circuit is connected to a 20V (or other voltage) power supply 302 and to ground 322 that is intended to provide the necessary voltage for the transducer C1 170. Within the circuit, a current source G1 304 drives current to constantly charge the trickle capacitor C3 318. In parallel with the trickle capacitor C3 318 is an optional Zener diode 320 used to limit the voltage applied to transducer C1 314. In parallel with the transducer C1 314 is an optional Zener diode 312 used to limit the voltage applied to capacitor C3 318. The Zener diodes 312 and 320 are optionally used to limit voltages created during circuit operation. In the integrated circuit implementation described in Figure 1 and Figure 2 the functionality of the optional Zener diodes can be performed with on-chip voltage detection circuits known to anyone of ordinary skill in the art.

[00107] Switch S1 306 is used to charge the transducer from 0V to 20V. When switch S1 306 is closed charge accumulated on the capacitor C3 318 is transferred onto the transducer C1 314. Current from the current source G1 304 is also provided to both capacitor C3 318 and transducer C1 314. Flow of charge between capacitor C3 318 and transducer C1 314 will cease when the same

voltage is developed across both components. Switch S1 306 would only close when the transducer C1 314 is at 0V when driven to create haptic feedback.

[00108] When switch S1 306 is open the current from the current source G1 304 flows into the capacitor C3 318, charging the capacitor towards 20V. As the voltage across capacitor C1 314 approaches 20V the current source G1 304 will begin to collapse.

[00109] Switch S2 310 is used to discharge the transducer C1 314 from 20V to 0V. When switch S2 310 is closed the charge on the transducer C1 314 is discharged into VSS or ground 322.

[00110] When switch S2 310 is open the transducer C1 314 is prevented from discharging into VSS or ground 322.

[00111] When switch S3 308 is closed the local storage capacitor C2 316 is connected into the circuit. Switch S3 308 is only closed when both switch S1 306 and switch S2 310 are open.

[00112] When switch S3 308 is closed and the transducer C1 314 voltage is 20V then the charge stored on the transducer C1 314 is transferred to local storage capacitor C2 316. Flow of charge between transducer C1 314 and local storage capacitor C2 316 will cease when the same voltage is developed across both components.

[00113] When switch S3 308 is closed and the transducer C1 314 voltage is 0V then the charge stored on the local storage capacitor C2 316 is transferred to transducer C1 314. Flow of charge between transducer C1 314 and local storage capacitor C2 316 will cease when the same voltage is developed across both components.

[00114] When discharging the transducer C1 314 into the local storage capacitor C2 316 there will be a residual charge retained on the transducer C1 314 requiring switch S3 308 to be opened and switch S2 310 to be closed to fully discharge transducer C1 314 to 0V.

[00115] When charging the transducer C1 314 from the local storage capacitor C2 316 the voltage on transducer C1 314 may not reach 20V depending on the

available charge on local storage capacitor C2 316. Switch S3 308 will be opened and switch S1 306 closed to transfer the required charge from capacitor C3 318 to charge transducer C1 314 to 20V.

[00116] When switch S3 308 is open the local storage capacitor C2 316 is isolated to retain charge needed for subsequent charging of transducer C1 314 to 20V.

[00117] Capacitor C3 318 and current source G1 304 isolate switching of the transducer C1 314 from the 20V power supply 302. Switches S1 306, S2 310 and S3 308 are used for the rapid transfer of charge between transducer C1 314, local storage capacitor C2 316 and trickle capacitor C3 318. The on resistance of switches S1 306, S2 310 and S3 308 must be minimized towards a few Ohms to maintain efficiency of the switching activity as charge is moved between transducer C1 314, local storage capacitor C2 316 and trickle capacitor C3 318. Implementation of low on-resistance switches in integrated circuit form requires the use of very large transistors and switches S1 306, S2 310 and S3 308 constitute the majority of the silicon die area for the circuit shown in Figure 3 300.

[00118] VI. Conclusion

[00119] While the foregoing descriptions disclose specific values of voltage, capacitance and current, any other specific values may be used to achieve similar results. Further, the various features of the foregoing embodiments may be selected and combined to produce numerous variations of improved haptic systems.

[00120] In the foregoing specification, specific embodiments have been described. However, one of ordinary skill in the art appreciates that various modifications and changes can be made without departing from the scope of the invention as set forth in the claims below. Accordingly, the specification and figures are to be regarded in an illustrative rather than a restrictive sense, and all such modifications are intended to be included within the scope of present teachings.

[00121] Moreover, in this document, relational terms such as first and second, top and bottom, and the like may be used solely to distinguish one entity or action

from another entity or action without necessarily requiring or implying any actual such relationship or order between such entities or actions. The terms “comprises,” “comprising,” “has,” “having,” “includes,” “including,” “contains,” “containing” or any other variation thereof, are intended to cover a non-exclusive inclusion, such that a process, method, article, or apparatus that comprises, has, includes, contains a list of elements does not include only those elements but may include other elements not expressly listed or inherent to such process, method, article, or apparatus. An element preceded by “comprises ...a”, “has ...a”, “includes ...a”, “contains ...a” does not, without more constraints, preclude the existence of additional identical elements in the process, method, article, or apparatus that comprises, has, includes, contains the element. The terms “a” and “an” are defined as one or more unless explicitly stated otherwise herein. The terms “substantially”, “essentially”, “approximately”, “about” or any other version thereof, are defined as being close to as understood by one of ordinary skill in the art. The term “coupled” as used herein is defined as connected, although not necessarily directly and not necessarily mechanically. A device or structure that is “configured” in a certain way is configured in at least that way, but may also be configured in ways that are not listed.

[00122] The Abstract of the Disclosure is provided to allow the reader to quickly ascertain the nature of the technical disclosure. It is submitted with the understanding that it will not be used to interpret or limit the scope or meaning of the claims. In addition, in the foregoing Detailed Description, it can be seen that various features are grouped together in various embodiments for the purpose of streamlining the disclosure. This method of disclosure is not to be interpreted as reflecting an intention that the claimed embodiments require more features than are expressly recited in each claim. Rather, as the following claims reflect, inventive subject matter lies in less than all features of a single disclosed embodiment. Thus the following claims are hereby incorporated into the Detailed Description, with each claim standing on its own as a separately claimed subject matter.

CLAIMS

We claim:

1. A circuit comprising:

a voltage source;

a voltage sink;

a current source having a current source first end and a current source second end;

a trickle capacitor having a trickle capacitor first end and a trickle capacitor second end;

a storage capacitor having a storage capacitor first end and a storage capacitor second end;

a haptic system transducer having a transducer first end and a transducer second end;

a first switch having a first switch first end and a first switch second end;

a second switch having a second switch first end and a second switch second end;

a third switch having a third switch first end and a third switch second end;

wherein the current source first end is connected to the voltage source;

wherein the current source second end is connected to the trickle capacitor first end and is connected to the first switch first end;

wherein the first switch second end is connected to the third switch first end and is connected to the second switch first end and is connected to the transducer first end;

wherein the third switch second end is connected to the storage capacitor first end;
and

wherein the second switch second end, the transducer second end, the storage capacitor second end and the trickle capacitor second end are connected to the voltage sink.

2. The circuit as in claim 1 wherein the current source, the first switch, the second switch and the third switch are located on an integrated circuit.

3. The circuit as in claim 2, wherein the voltage source, the voltage sink, the transducer, the storage capacitor, and the trickle capacitor are discrete components.

4. The circuit as in claim 2, wherein the storage capacitor and the trickle capacitor are located on the integrated circuit.

5. The circuit as in claim 2, wherein when the first switch is closed, charge accumulated on the trickle capacitor is transferred onto the transducer.

6. The circuit as in claim 2, wherein when the first switch is open the current from the current source flows into the trickle capacitor charging the capacitor towards the amount of the voltage source.

7. The circuit as in claim 2, wherein when the second switch is closed the charge on the transducer is discharged into the voltage sink.

8. The circuit as in claim 2, wherein when the second switch is open the transducer is prevented from discharging into the voltage sink.

9. A circuit comprising:
a voltage source;

a voltage sink;

a current source having a current source first end and a current source second end;

a trickle capacitor having a trickle capacitor first end and a trickle capacitor second end;

a first storage capacitor having a first storage capacitor first end and a first storage capacitor second end;

a first haptic system transducer having a first transducer first end and a first transducer second end;

a first switch having a first switch first end and a first switch second end;

a second switch having a second switch first end and a second switch second end;

a third switch having a third switch first end and a third switch second end;

a second storage capacitor having a second storage capacitor first end and a second storage capacitor second end;

a second haptic system transducer having a second transducer first end and a second transducer second end;

a fourth switch having a fourth switch first end and a fourth switch second end;

a fifth switch having a fifth switch first end and a fifth switch second end;

a sixth switch having a sixth switch first end and a sixth switch second end;

wherein the first current source first end is connected to the voltage source;

wherein the first current source second end is connected to the trickle capacitor first end and is connected to the first switch first end, and is connected to the fourth switch first end;

wherein the first switch second end is connected to the third switch first end and is connected to the second switch first end and is connected to the first transducer first end;

wherein the fourth switch second end is connected to the sixth switch first end and is connected to the fifth switch first end and is connected to the second transducer first end;

wherein the third switch second end is connected to the first storage capacitor first end;

wherein the sixth switch second end is connected to the second storage capacitor first end;

wherein the second switch second end, the fifth switch second end, the first transducer second end, the second transducer second end, the first storage capacitor second end, the second storage capacitor second end, and the trickle capacitor second end are connected to the voltage sink.

10. The circuit as in claim 9 wherein the current source, the first switch, the second switch, the third switch, the fourth switch, the fifth switch and the sixth switch are located on an integrated circuit.

11. The circuit as in claim 8, wherein when the first switch and the fourth switch are closed charge accumulated on the trickle capacitor is transferred onto the first transducer and second transducer.

12. The circuit as in claim 8, wherein the first switch and the fourth switch are open, the current from the current source flows into the trickle capacitor.

13. The circuit as in claim 8, wherein the second switch and the fifth switch are closed, the charge on the first transducer and the second transducer is discharged into the voltage sink.

14. The circuit as in claim 8, wherein the second switch and the fifth switch are open, the first transducer and the second transducer are prevented from discharging into the voltage sink.

15. A circuit comprising:

a voltage source;

a voltage sink;

a current source having a current source first end and a current source second end;

a trickle capacitor having a trickle capacitor first end and a trickle capacitor second end;

a storage capacitor having a storage capacitor first end and a storage capacitor second end;

a haptic system transducer having a transducer first end and a transducer second end;

a first switch having a first switch first end and a first switch second end;

a second switch having a second switch first end and a second switch second end;

a third switch having a third switch first end and a third switch second end;

a first Zener diode having a first anode and a first cathode;

a second Zener diode having a second anode and a second cathode;

wherein the current source first end is connected to the voltage source;

wherein the current source second end is connected to the trickle capacitor first end and is connected to the first switch first end and is connected to the first cathode;

wherein the first switch second end is connected to the third switch first end and is connected to the second switch first end and is connected to the second cathode and is connected to the transducer first end;

wherein the third switch second end is connected to the storage capacitor first end; and

wherein the second switch second end, the transducer second end, the storage capacitor second end, the first anode, the second anode and the trickle capacitor second end are connected to the voltage sink.

16. The circuit as in claim 15, wherein when the first switch is closed, charge accumulated on the trickle capacitor is transferred onto the transducer.

17. The circuit as in claim 16, wherein when the first switch is closed, current from the current source is also provided to the trickle capacitor and the transducer.

18. The circuit as in claim 15, wherein when the first switch is open the current from the current source flows into the trickle capacitor.

19. The circuit as in claim 15, wherein when the second switch is closed, the charge on the transducer is discharged into the ground sink.

20. The circuit as in claim 15, wherein when the second switch is open, the transducer is prevented from discharging into the ground sink.

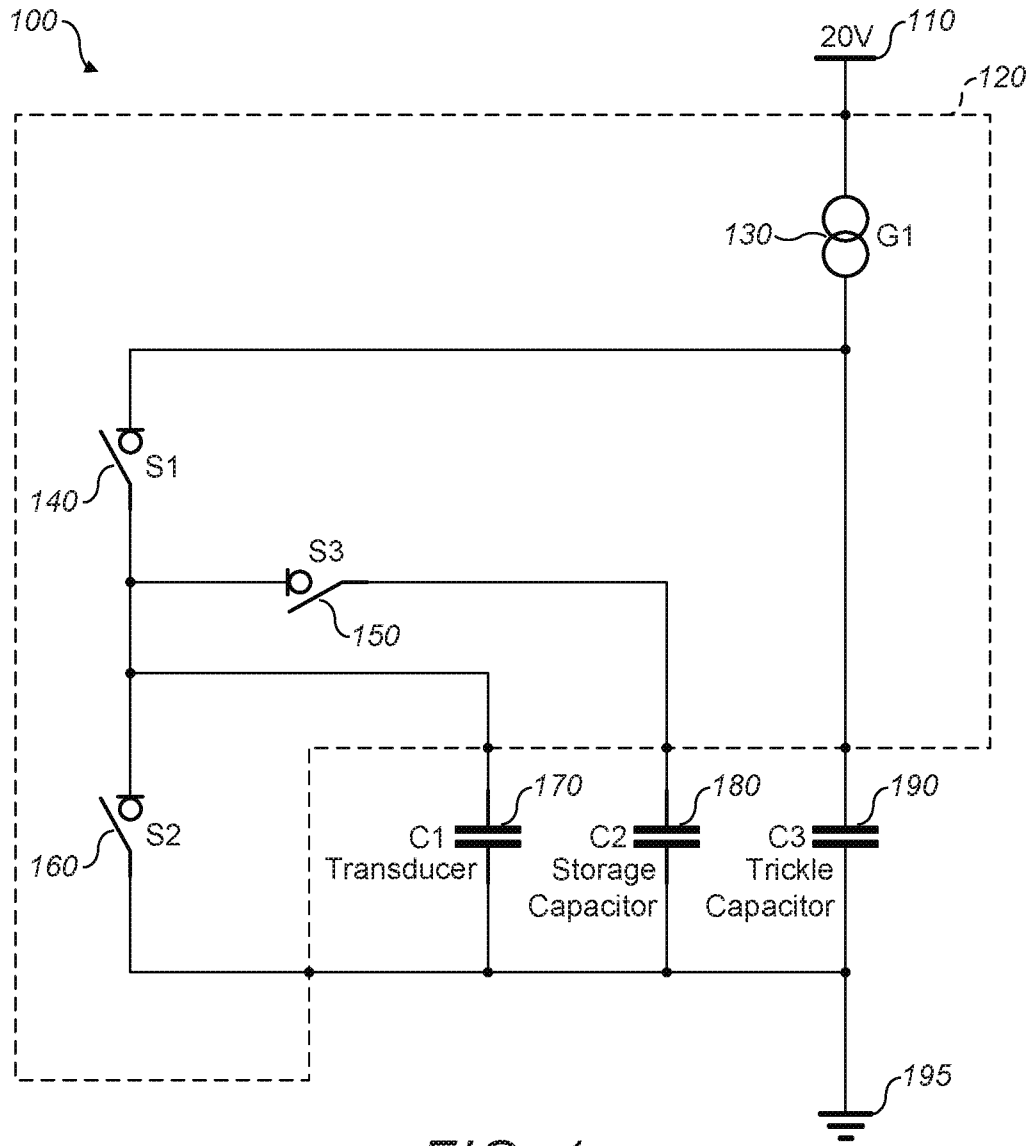


FIG. 1

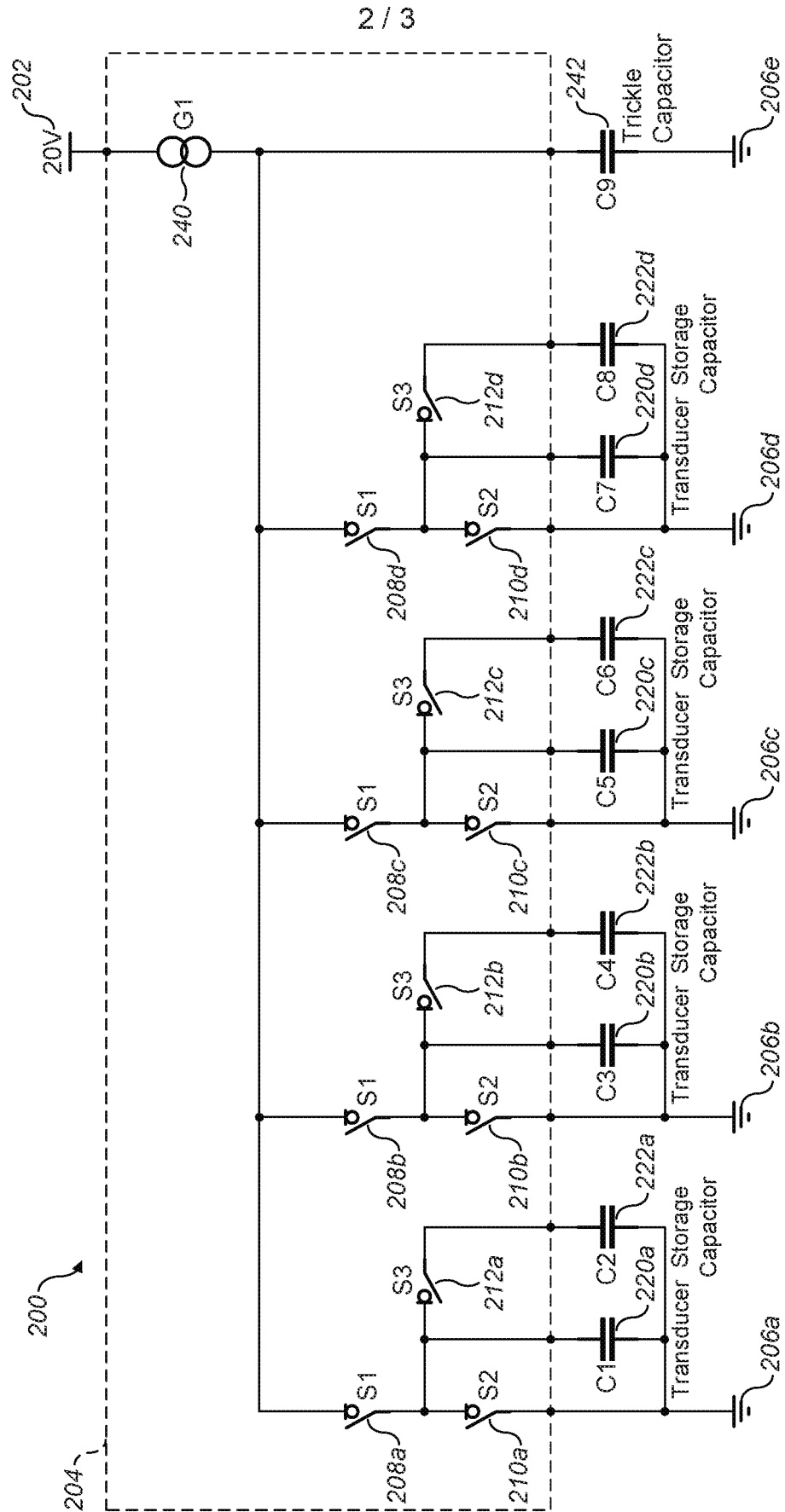


FIG. 2

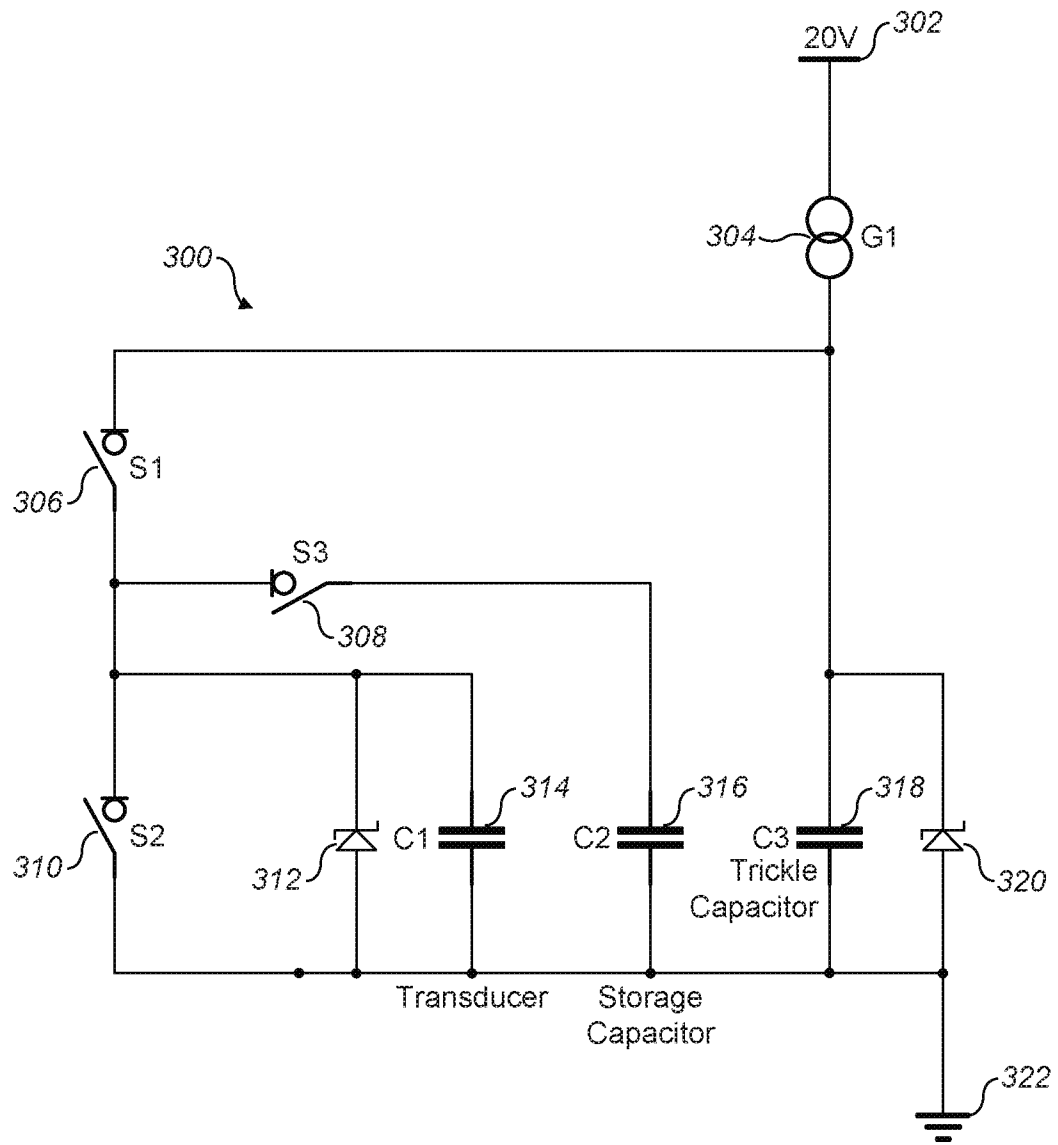


FIG. 3

INTERNATIONAL SEARCH REPORT

International application No
PCT/GB2017/053880

A. CLASSIFICATION OF SUBJECT MATTER
INV. B06B1/02 H01L41/04
ADD.
According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED
Minimum documentation searched (classification system followed by classification symbols)
B06B H01L
Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)
EPO-Internal, WPI Data

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
A	US 2014/265572 A1 (SIEDENBURG CLINTON T [US]) 18 September 2014 (2014-09-18) abstract claim 1 paragraph [0004] paragraph [0014] paragraph [0027] - paragraph [0028] paragraph [0031] figure 1	1-20
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Further documents are listed in the continuation of Box C.

See patent family annex.

* Special categories of cited documents :

- "A" document defining the general state of the art which is not considered to be of particular relevance
- "E" earlier application or patent but published on or after the international filing date
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- "O" document referring to an oral disclosure, use, exhibition or other means
- "P" document published prior to the international filing date but later than the priority date claimed

"T" later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention

"X" document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone

"Y" document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art

"&" document member of the same patent family

Date of the actual completion of the international search 9 March 2018	Date of mailing of the international search report 21/03/2018
Name and mailing address of the ISA/ European Patent Office, P.B. 5818 Patentlaan 2 NL - 2280 HV Rijswijk Tel. (+31-70) 340-2040, Fax: (+31-70) 340-3016	Authorized officer Tympe1, Jens

INTERNATIONAL SEARCH REPORT

International application No
PCT/GB2017/053880

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